TECHNIQUES FOR PRELOADING DATA INTO MEMORY ON PROGRAMMABLE CIRCUITS

ABSTRACT OF THE DISCLOSURE

[0071]   Techniques for preloading data into memory blocks on a programmable circuit are provided. Memory blocks on the a programmable circuit each have dedicated circuitry that loads data into the memory block. The dedicated circuit also generates memory addresses used to load the data into the memory block. The dedicated circuitry associated with each memory block reduces demand on the routing resources. A user can preload data into the memory blocks prior to user mode. A user can also prevent data from being preloaded into one or more of the memory blocks prior to user mode. By allowing the user to program some or all of the memory blocks prior to user mode, the time needed to program the memory blocks prior to user mode can be substantially reduced.

23312506 v1